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**IN THE U.S. PATENT AND TRADEMARK OFFICE**

Re U.S. Patent Application of:

APPLICANT: Zhao et al.

SERIAL NO.: 09/823,159

FILING DATE: 03/30/2001

EXAMINER: Patel, Nimesh G.

ART UNIT: 2112

ATTORNEY'S DOCKET NO.: 871.0013.USU

TITLE: PROGRAMMABLE CPU/INTERFACE BUFFER STRUCTURE USING DUAL  
PORT RAM

**RECEIVED**

AUG 18 2004

Commissioner for Patents

**BOX AF**

Technology Center 2100

Alexandria, VA 22313

**RESPONSE TO OFFICE ACTION**

Sir:

This paper is herewith filed in response to the second Examiner's Office Action mailed on 05/14/2004 for the above-captioned U.S. Patent Application. This Office Action is a final rejection of claims 1-20 (all of the pending claims). This paper is deemed to be filed within the shortened statutory period and no petition or fee for an extension of time is required. However, should the undersigned attorney be mistaken, please consider this a petition for any extension of time that may be required to maintain the pendency of this Patent Application, and charge deposit account no.: 50-1924 for any required fee deficiency.

